



Express Mail No. EV 533 737 361 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent: Ngyuen et al.

Patent No.: 7,088,127 B2

Issued: August 8, 2006

For: *Adaptive Impedance Output Driver Circuit* Attorney Docket No: 60809-0127-US

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Upon review of the subject patent, Patentee's attorney discovered five (5) errors on the Title Page and in the claims on the part of the Patent Office. Patentee's attorney requests correction of the issued patent, a Form PTO 1050 Certificate of Correction is enclosed in duplicate.

No fee is believed to be due with this paper; however, the Commissioner is authorized to charge any required fees or credit any overpayments to our Deposit Account No. 50-0310 (order no. 60809-0127-US). A copy of this sheet is enclosed.

Respectfully submitted,

Date: August 24, 2006

Gary S. Williams 31,066 (Reg. No.)

**MORGAN, LEWIS & BOCKIUS** LLP

2 Palo Alto Square  
3000 El Camino Real, Suite 700  
Palo Alto, California 94306  
(650) 843-4000

**Certificate**  
**AUG 30 2006**  
**of Correction**

**AUG 30 2006**

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 7,088,127 B2  
 DATED : August 8, 2006  
 INVENTOR(S) : Ngyuen et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page:

Item (12), the inventor name of "Ngyuen et al." should read --Nguyen et al.--

Item (73), Inventors: the name "Huy M. Ngyuen" should read --Huy M. Nguyen--

In the Claims:

Column 12, line 45, after "signal" please delete ";" and please insert --in accordance with topology information; and--.

Column 12, line 51, after "voltage" please delete "." and insert --;--.

Column 12, after line 51, please insert as a new subparagraph --wherein said level shift means includes a first transistor means for providing electrostatic discharge protection, and wherein said first transistor means is coupled to a voltage output of said compensator means, and a second transistor means for outputting a signal with an output drive current in response to said input signal.--

MAILING ADDRESS OF SENDER:  
 MORGAN, LEWIS & BOCKIUS LLP  
 2 Palo Alto Square  
 3000 El Camino Real, Suite 700  
 Palo Alto, CA 94306  
 (650) 843-4000

FORM PTO 1050

PATENT NO. 7,088,127 B2

No. of add'l. copies  
 @ 30¢ perpage

==>

AUG 30 2006

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 7,088,127 B2  
 DATED : August 8, 2006  
 INVENTOR(S) : Ngyuen et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page:

Item (12), the inventor name of "Ngyuen et al." should read --Nguyen et al.--

Item (73), Inventors: the name "Huy M. Ngyuen" should read --Huy M. Nguyen--

In the Claims:

Column 12, line 45, after "signal" please delete "," and please insert --in accordance with topology information; and--.

Column 12, line 51, after "voltage" please delete "." and insert --;--.

Column 12, after line 51, please insert as a new subparagraph --wherein said level shift means includes a first transistor means for providing electrostatic discharge protection, and wherein said first transistor means is coupled to a voltage output of said compensator means, and a second transistor means for outputting a signal with an output drive current in response to said input signal.--

MAILING ADDRESS OF SENDER:  
 MORGAN, LEWIS & BOCKIUS LLP  
 2 Palo Alto Square  
 3000 El Camino Real, Suite 700  
 Palo Alto, CA 94306  
 (650) 843-4000

FORM PTO 1050

PATENT NO. 7,088,127 B2

No. of add'l. copies  
 @ 30¢ perpage

⇒

AUG 30 2006